

~~depleted~~ ~~depieted~~, so that an ideal on/off current characteristic of the selection transistor 3 can be achieved. The subthreshold transconductance of such a transistor can be higher than a conventional transistor. A higher current can be achieved with a significantly reduced voltage at the gate. This affords advantages over conventional memory types, for instance, a higher current consumption and a smaller area taken by the circuits.

Please replace the paragraph beginning on page 11, line 3 with the following amended paragraph:

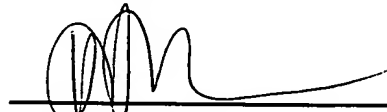
The photolithographic patterning of the mask and etching of the capacitor trenches can be followed by the deposition of the capacitor dielectric (for instance, a nitride, oxide, an aluminum oxide, etc.) and, on the latter, the inner capacitor electrode can be made, for example, of heavily n-doped polysilicon. The material of the inner capacitor electrode can be etched back at most as far as the lower edge of the buried insulation layer 11 of the semiconductor substrate 20. The capacitor dielectric 13 can then be removed at the level of the web.

#### **REMARKS**

Favorable consideration of this application in view of the above amendments and the following remarks is respectfully requested. By this amendment, the specification has been amended to correct minor translation inaccuracies. Applicants submit that no new matter has been added and formal notice of such is solicited. Currently, claims 1-18 are pending.

Applicants respectfully submit that all pending claims are in condition for allowance and formal notice of such is solicited. If the Examiner has any questions, the Examiner is respectfully requested to contact the undersigned at the number below.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Heather Morin', written over a horizontal line.

Heather Morin  
Reg. No. 37,336

EDELL, SHAPIRO & FINNAN, LLC  
1901 Research Boulevard, Suite 400  
Rockville, Maryland 20850-3164  
(301) 424-3640

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